



पश्चिमबंग पश्चिम बंगाल WEST BENGAL
AGREEMENT
Between

80AB 990665

J.B. INSTITUTE OF ENGINEERING AND TECHNOLOGY, HYDERABAD
And
JADAVPUR UNIVERSITY, KOLKATA

This agreement is executed on Date 1st April 2023

BETWEEN

J.B Institute of Engineering and Technology, Bhaskar Nagar, Yenkapally Village, Moinabad Mandal, R.R. Dist.-75, Telangana State (hereinafter referred to as JBIET) of the FIRST PART

AND

Jadavpur University, a body corporate under JU Act, 1981, having its campus at Jadavpur University, Kolkata – 700032, India (hereinafter referred to as JU) of the SECOND PART.

WHEREAS JU, as part of achieving one of its objectives, has undertaken to train and collaborate group of young researchers in training on Device, VLSI and Embedded system related topics to serve the community.

AND WHEREAS the representative of JBIET approached the Co-ordinator, IC Design and Fabrication Centre under Department of Electronics & Tele-communication Engineering of JU to have collaboration on Device, VLSI and Embedded system related

topics for training, joint PhD, research and development projects, internship (hereinafter referred to as the course) to finalize the terms of this agreement.

AND WHEREAS pursuant to the said discussion, JU has agreed to offer its facility for joint collaboration, patent filing, research and development, joint PhD registration and manpower training **from 1st April 2023 to 31st March 2027** for a period of **four (4)** years.

IN WITNESS WHEREOF, the parties hereto agree to the terms and conditions as hereunder written.

- a) The research collaboration will include SCI Journal Publications in collaboration with faculty members from BOTH PARTIES.
 - b) Request for joint Ph. D program where, initially, it is request to JU to register one faculty member as Its Ph.D candidate from JBIET in joint supervision, whereas the faculty member will work in Bio Electronics laboratory in JBIET, Hyderabad and will report to his/her supervisor in JU as/when necessary. If the faculty can satisfy the supervisor through quality research, JU shall proceed for other faculty registration.
 - c) If any patent is filled, the patent filling cost will be equally shared by the both parties depending the number of participants from both parties.
 - d) There will be exchange visit between the parties where faculty members and research scholars can visit laboratory. Local hospitality will be taken care by Host Institutions.
 - e) Any Experimental facilities can be shared between two parties according to their mutual understanding.
 - f) Faculty Staff members from both parties can attend conferences/workshops etc. upon mutual understanding and self financial support.
 - g) In the internship programme, IC Design & Fabrication Centre, Department of Electronics & Telecommunication Engineering, JU will offer internship (long term and short term) on Microelectronics and VLSI technology and/or Embedded systems and IoT. The said course will be a part time course in nature. For short term course, the course will be six weeks in nature.
- 3] There shall be a **Course Committee** consisting of the following members:
- (i) Co-ordinator, IC Centre, JU
 - (ii) Co Coordinator, IC Centre, JU
 - (iii) HOD, Dept. of ETCE, JU
 - (iv) Principal/Director, JBIET, Hyderabad.
 - (v) HOD, Dept. of ECE, JBIET, Hyderabad.
 - (vi) Coordinator, Bioelectronics laboratory, JBIET
- 4] For the short term training course the payment will be as per Table-1. However, the table is for offline mode of training. Table-2 shows the online mode of training. For long term course, the modalities and payment process will be decided as and when necessary depending on number of students.

- 5] The Course Committee shall look after the following:
- (i) Framing of syllabus and making future amendments/additions/alternations etc., as and when necessary.
 - (ii) Usage of Patent and its fees structure.
 - (iii) Constitute Research advisory committee for Joint PhD and will monitor six months progress of the candidate.
 - iv) Will monitor about the progress of joint collaborative research work
- 6] IC Design and Fabrication Centre, JU, shall conduct all activities relating to examination corresponding to their respective teaching load. These include printing of question papers, conducting of examination.
- 7] Format of the Course certificate shall be approved by the Course Committee. The authorized representatives of JU (Head of the Department, ETCE Dept., JU and Co-ordinator of IC Design and Fabrication Centre, JU) jointly sign the certificate to be issued to the students. The certificate shall be prepared by IC Design and Fabrication Centre, JU and shall be distributed by IC Design and Fabrication Centre, JU.

COLLABORATION

- (i) The entire financial obligation, both administrative and academic – expenditure for running the aforesaid course – shall be met up from the course fees collected from the students admitted to the course.
- (ii) IC Design and Fabrication Centre shall collect the fees from the students by DD, in favour of "**REGISTRAR, JADAVPUR UNIVERSITY**" and shall pay to JU after deduction Overhead 10% of the fees & 18% GST paid to JU and release the balance in favour of "**IC DESIGN & FABRICATION CENTRE, JU**".
- (iii) The cost of marketing the course including the cost of marketing personnel, advertisement(s) in print and audio / visual media, if any, brochures, handouts, leaflets, etc. shall be met up from the course fees collected from the students admitted to the course. The legal expenses, if any, incurred for defending the suit(s) instituted by any of the students and/or other partie(s) in connection with the said Course shall also be debited to this account.
- (iv) The expenditure as may be incurred for paying remuneration to the Faculty Members / Teachers / Technical staff, Attendants etc should be met up from the course fees collected from the students admitted to the course.
- (v) For patenting, both party should mutually agree about IPR policy of respective institutions and payment terms will be as per that IPR policies.
- Vii) For joint research and development projects financial expenditure will be taken care by the PI of that project. However, laboratory usage fees, consumables and

purchase of instrument will be as per the budget (institute wise breakup) mentioned in proposal.

This agreement will remain in force initially for **Four year** from the date of signing of this agreement. This agreement may be renewed on its expiry by a written request by the 1st party at least two months prior to the expiry. The first or any subsequent annual renewal shall not take place, if notice in writing of its intention not to renew this agreement is given by either party to the other at least two months in advance.

If any dispute arises between the parties of the FIRST AND SECOND PART the same shall be decided upon arbitration of the Vice-Chancellor, Jadavpur University and his/her decision shall be final.

About the Programme (offline mode)

- 1] The Centre will train the students of the department in the domain of VLSI Design & Microelectronics Technology/ Embedded systems and IoT for a duration of 100 hours (approximately) per session probably in the inter semester gap. The cost of the training will be borne by the Institute (J.B. Institute of Engineering & Technology, Hyderabad.) as per the following rate:-

Table - 1: Course Fee Structure (Offline mode)

No. of Students	Cost of the Training Programme
61 - 80	@ Rs. 5,310/- [Rs. 4,500/- + 18% GST] Per head
21 - 60	@ Rs. 5,900/- [Rs. 5,000/- + 18% GST] Per head
01 - 20	@ Rs. 8,260/- [Rs. 7,000/- + 18% GST] Per head

Table - 2 (Online mode)

No. of Students	Cost of the Training Programme
61 - 80	@ Rs. 2,000/- [Rs. 1695 /- + 18% GST] Per head
21 - 60	@ Rs. 2,500/- [Rs. 2118 /- + 18% GST] Per head
01 - 20	@ Rs. 3,000/- [Rs. 2542 /- + 18% GST] Per head

- 4] Two staff (Faculty members and/or Technical support staff) of the department per year can pursue training at the centre at free of cost, provided two batches of at least 10 students attending training program.
- 5] Faculty Development Program for the favour of the 1st party i.e. (JBIET, Hyderabad) can be organized by IC Design & Fabrication Centre, JU where the man power and resource of the department and the centre will be utilized. Modality of the program and fees for the Faculty Development Programme will be decided based on the requirement of the program.

- 6] The agreement is valid upto the academic session (2026-2027) but can be terminated by either side with valid justification after a particular academic session (not in the midsession).

FORCE MAJEURE

- (i) In the event either party cannot perform its respective obligation under the Agreement due to Force Majeure, the corresponding obligations of the other party may at the instance and notice of such either party be suspended for a similar period of time.
- (ii) Force Majeure shall include but not limited to acts of God, regulation of Government, earthquakes, floods, epidemic, war, revolution, strikes, lockouts, explosion, computer virus attack and natural calamities.

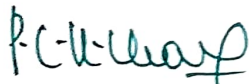
Signed on behalf of Second Part
/ Jadavpur University



Prof. Sayan Chatterjee
Co-ordinator, IC Centre
SECOND PART

Prof. Sayan Chatterjee
Co-ordinator
IC Design & Fabrication Centre
Dept. of ETCE., JU

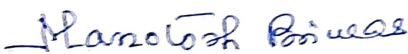
Signed on behalf of First Part
/ J.B Institute of Engineering &
Technology, Hyderabad


Dr. P.C. KRISHNAMACHARY

Principal/Director, J.B Institute of
Engineering & Technology, Hyderabad
FIRST PART

PRINCIPAL
J.B. INSTITUTE OF
ENGINEERING & TECHNOLOGY
Bhaskar Nagar, Yenkapally (V)
Moinabad (M), R.R. Dist.-500 075

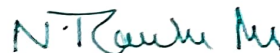
Witnesses:




1. HOD, ETCE
Jadavpur University.

MANOTOSH BISWAS

Professor and Head
Electronics and Telecommunication Engineering
Jadavpur University, Kolkata - 32



1. HOD, ECE, J.B. Institute of
Engineering & Technology, Hyderabad

- 
2. Co-ordinator, Bioelectronics
Laboratory, J.B. Institute of
Engineering & Technology, Hyderabad