



IEEE SB JBIET

J.B. INSTITUTE OF ENGINEERING AND TECHNOLOGY

(UGC AUTONOMOUS)

Accredited by NAAC, NBA, Approved by AICTE & Permanently affiliated to JNTUH

Webinar

On

Job opportunities in VLSI/ Semiconductor Industry

(11th Febuary 2025)

Resource Person

Arun, Physical Design Engineer

Takshila Institute of VLSI Technologies

Co-ordinator

B. Shravan Kumar

HOD-ECE

Dr. Towheed Sulthana



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Bhaskar Nagar, Yenkapally (V), Moinabad (M), P.O. Himayathnagar, R.R. District, Hyderabad-500075.

Date- 10/02/2025

CIRCULAR

All the III B.tech -II Semester Students are informed that Mr.Arun, Physical Design Engineer from Takshila Institute of VLSI Technologies will be delivering a Webinar on 11th Febuary 2025 & Interact with Students to deliver a Lecture on “Job opportunities in VLSI/ Semiconductor Industry

It is Excellent Opportunity for Student to interact and take advice for Job opportunities in VLSI/ Semiconductor Industry

Venue : JBIET Main Block Placement Seminar Hall

Dr. Towheed Sultana,

Professor & HOD - ECE

copy to : PRINCIPAL



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Bhaskar Nagar, Yenkapally (V), Moinabad (M), P.O. Himayathnagar, R.R. District, Hyderabad-5000075.
Department of Electronics And Communication Engineering

Department of Electronics and Communication Engineering

Dt :12-02-2025

Report on Webinar --- Job opportunities in VLSI / Semiconductor Industry

Name of the Event : **Job opportunities in VLSI/ Semiconductor Industry**

Date & Time: **11.02.2025 & 1:30 PM**

Venue: Placement Seminar Hall -1

No. of Participants : ECE students (90) + Faculty (04)

Resource Person : Mr.Arun, Physical Design Engineer from Takshila Institute of VLSI Technologies, Hyderabad

Objective of the event : To create awareness about the job opportunities in VLSI/
Semiconductor Industry.

Dr. Towheed Sultana, HOD Department of ECE, delivered the felicitation address and welcomed the resource person Mr.Arun, Physical Design Engineer from Takshila Institute of VLSI Technologies, Bangalore and instructed the students to get benefited.

Physical design engineer, Takshila, gave an elegant lecture on VLSI and Semiconductors. He explained in detail about the opportunities in various companies like WIPRO, BOSCH etc. annual salary packages in VLSI/ Semiconductor industry. He motivated the students by giving the methodology, the way how to enter into the VLSI industry. He also covered the following topics.

- How a chip interior looks like
- Semiconductor wafer
- How integration is made possible
- VLSI/ Semiconductor industry growth
- VLSI/ Semiconductor companies
 - Top 20 product based companies
- Established VLSI service companies
- Semiconductor IC making video

Outcome of event :

- Students have acquired the information on VLSI importance, and the present scenario of VLSI industries.
- Understand about the placement opportunities for the concepts of VLSI.
- Understand about the technical tools required for VLSI. This webinar has covered the concepts of physical design , analog circuits design and their layout design, ASIC verification and the testing design.

Photographs





Geo-Tagging Camera

2025/02/11 15:30

Rangareddy, Telangana, India

87JX+53G, Telangana 500075, India

Lat 17.330237 Long 78.297837